An Immunoassay Platform Based on CMOS Hall Sensors

Turgut Aytur, P. Robert Beatty¹, Bernhard Boser
Dept. of Electrical Engineering & Computer Sciences, ¹Dept. of Molecular & Cellular Biology
University of California, Berkeley, CA 94720

Mekhail Anwar
Dept. of Electrical Engineering and Computer Sciences
Massachusetts Institute of Technology
Cambridge, MA 02139

Tomohiro Ishikawa
System Technology Research Center
Sharp Corporation
Chiba, Japan

ABSTRACT

We describe an immunoassay utilizing standard CMOS technology. An array of Hall sensors is used to detect the magnetic beads that serve as the assay signal. Electrical and magnetic modulation is employed to improve the sensitivity of the sensors. The devices receive two post-processing steps to improve sensitivity and biocompatibility. We have fabricated prototype devices using a 0.25-µm BiCMOS process, and have successfully detected anti-Hu IgG antibody at a concentration of 200pM.

INTRODUCTION

Diagnosis is an essential tool in the health care industry. The role of diagnosis is expanding, particularly within the context of screening and prevention. Infectious diseases are a major cause of death in the world, with HIV/AIDS, tuberculosis, and malaria responsible for approximately 5.7 million deaths in 1998 [1]. Rapid diagnosis is essential during epidemics for fast treatment and containment.

A dominant technology in diagnostics is the Enzyme-Linked Immunosorbent Assay (ELISA). Immunoassays, such as ELISA, are diagnostic tools that rely on the highly specific interaction of antibody binding to cognate antigen. In the ELISA, the detection of unknown antibody or antigen is signaled through an enzymatic label that activates a dye. ELISAs can be administered in a doctor’s office or hospital environment, allowing for cost effective diagnosis of a broad range of diseases. However, most ELISA tests require a basic laboratory environment and some staff training. For many infectious diseases, a rapid, precise quantitative measurement is required for accurate diagnosis, which is rarely possible in the field. Therefore, improvements in diagnosis should focus on rapid, cost-effective in-field or point-of-care testing.

Magnetically-Labeled Assays

The limitations in ELISA for quantified point-of-care diagnostics stem mostly from the enzymatic label. Recently, the use of micron-scale magnetic beads as labels has been described [2,3,4]. There are many advantages gained from using a magnetic label. First, there are no comparable sources of magnetic signal in biology, so the background is intrinsically low. Second, magnetic beads can be used to manipulate biological systems. The beads are specifically superparamagnetic, as they have very low remanence. When placed in a magnetic field, however, they generate an induced magnetic field. The magnetic beads can be made biologically active by coating the polystyrene encapsulation.

The magnetic label requires a different detection system than ELISA. The first reports of assays utilizing magnetic labels employed superconducting quantum interference devices as sensors [2]. These devices are highly sensitive to magnetic fields, but are not portable. More recently, a device has been developed based on giant magneto resistor (GMR) technology [3,4]. GMR devices are highly sensitive, and are now in mass production in computer disk-drive read heads. A simplified protocol for an assay involving this device is shown in Figure 1.

![Figure 1. Simplified magnetically-labeled assay.](image)

The gold surface received a coating protein. The test liquid was added (Figure 1b). Next, magnetic beads coated with an appropriate antibody against the target antigen were added (Figure 1c). Magnetic beads far from the sensor but still in solution will not affect the result. This assay used magnetic washing, where unbound beads were pulled from the sensor by a magnetic field (Figure 1d). An external magnetic field normal to the sensor surface was then applied. The induced field generated by bound beads was measured as a resistance change in the GMR sensor. The amount of change in resistance corresponds to the number of beads bound. This device has single magnetic bead sensitivity.

Improvements in cost and manufacturability of the magnetic sensors may broaden the applicability of magnetically-labeled assays. CMOS is the most widely manufactured semiconductor technology. It is an attractive substrate for MEMS devices due to manufacturing ease and potential as an intelligent substrate. The sensor can be easily integrated with other functionality. The small size of the sensor chips described in this paper implies that the microfabrication costs will not dominate the assay expense. It is therefore beneficial to utilize CMOS for the sensing technology.

THEORY

Hall sensors can be operated in current or voltage mode. A particular example of a current-mode device that exploits the MOS transistor structure is the dual-drain Hall FET. The differential output current of this device can be expressed as [5],

$$i_d = \frac{L}{2W} \mu_H GB_z I_{DS}$$  \hspace{1cm} (1)
where $W$ and $L$ are the channel width and length, respectively, of the device, $\mu_H$ is the Hall mobility, and $G$ is a geometric constant that accounts for current confinement at the device boundaries. In the low magnetic fields of this application, the Hall mobility is well represented by the carrier mobility. For a long-channel device operating in saturation, Eq. 1 can be rewritten as,

$$i_d \equiv \frac{\mu \text{eff}}{4} GB_e C_{ox} \left( V_{gs} - V_t \right)^2 \quad (2)$$

where $\mu_{\text{eff}}$ is the effective carrier mobility.

**Device Scaling**

In the analysis above, the applied field is assumed to be uniform and normal to the sensor surface. Magnetic beads produce a local, non-uniform field when placed in an external field. This field is well modelled by a magnetic dipole equation. The peak signal decays with cubic dependence on the height from the sensor plane. A simulation of the induced magnetic field, measured normal to a plane 5μm below the center of a 5-μm bead, is shown in Figure 2.

**Figure 2. Simulated magnetic field for a 5-μm magnetic bead in a 35-kA/m field, 5-μm for the surface.**

The total field integrates to zero, since the field strength is anti-symmetric about the y-axis. Therefore, a hall-sensor device that is much larger than the bead will have a smaller signal than a correctly scaled device. Consider the hypothetical situation shown in Figure 3. In these devices, the magnetic field is zero outside the shaded strip, and finite in it. Therefore, the current bends in the region with magnetic modulation, as represented in Figure 4.

**Figure 3. Illustration of current lines for two device sizes in the same non-uniform magnetic field. The larger device has a reduced output signal due to charge redistribution.**

**Figure 4. Illustration of magnetic and electrical modulation applied to the dual-drain NMOS sensor device. The modulated output signal appears at frequencies $\omega_e \pm \omega_m$.**

The gate-to-source voltage and magnetic field are applied as:

$$V_{gs} (t) = V_{DC} + V_{AC} \sin(\omega_e t) \quad (3)$$

$$H_{xy} (t) = H_0 \sin(\omega_m t) \quad (4)$$

Where $\omega_e$ and $\omega_m$ are the electrical and magnetic modulation frequencies, respectively. Combining eq. (2) with eqs. (3) and (4) defines the modulated output spectrum. In addition to DC and higher order terms, the differential drain current will contain the term:

$$i_d \propto \sin(\omega_e \pm \omega_m) \quad (6)$$

The electrical modulation frequency can be selected such that thermal noise, rather than flicker noise, dominates. The magnetic modulation is desirable to separate the wanted signal from carrier leakage (dashed line in Figure 4). Carrier leakage results from a variety sources, including device leakage and parasitic coupling, and results in a limit on the minimum detectable signal.

**Flicker Noise**

CMOS devices have poor low-frequency noise properties due to flicker or 1/f noise. The noise spectral density, as the name suggests, is inversely proportional to frequency. The consequence of this is that signals at DC do not benefit from narrowing of the noise bandwidth, eliminating the trade-off between bandwidth and Signal-to-Noise Ratio (SNR). Thus, there exists a minimum detectable signal, independent of noise bandwidth.

For conventional magnetic sensing applications, the signal frequency is either not known or assumed to be at DC. However, in this application the excitation frequency of the external magnetic field is limited only by practical constraints of electromagnets. This improves the signal detection in two ways: First, a band-pass filter can be employed to restore the trade-off between bandwidth and SNR. Second, the signal can be moved to a frequency of lower spectral noise density. Unfortunately, there is still approximately one order of magnitude difference between the flicker noise corner frequency of the MOS hall device and the maximum frequency to practically operate the electromagnetic excitation. To overcome this, we combine electrical modulation and magnetic modulation, as represented in Figure 4.

**DESIGN AND FABRICATION**

**Sensor Chip**

The sensor chips were fabricated in Agere Systems (formerly Lucent Technologies-Microelectronics) 0.25-μm single-poly 5-
metal BiCMOS process. Each Hall device is implemented as a 6-µm x 6-µm dual-drain NMOS device. The drains are separated by a 1-µm x 1-µm field-oxide region. Each sensor consists of two matched devices that source current in opposite directions (Figure 5), so that uniform magnetic fields are rejected.

![Figure 5](image)

**Figure 5.** Two Hall devices source current in opposite directions, reducing uniform fields by 30-40 dB.

This reduces the leakage signal from the excitation field by 30-40 dB. This leakage signal occurs due to misalignment of the excitation field with the sensor surface.

The sensor array consists of 32 x 8 sensor elements. Figure 6 shows a portion of the sensor array.

![Figure 6](image)

**Figure 6.** A Portion of the sensor array with 3-µm magnetic beads. The Cr/Au layer was not applied to allow viewing of sensor detail.

Each element is addressable via a shift register. The drain current of the selected sensing device is converted to a voltage before being amplified by a bipolar transconductance amplifier. This balanced, current-mode output is sent off chip.

Due to a chip area restriction of 1mm x 1mm, additional metalization post-processing was necessary to move bond pads away from the sensing area. This allows wire bonding to the chip outside of the fluid vessel. This part of the post-processing will not be described, as future prototypes will accomplish this with the standard metallization.

Devices are post-processed in subsections of the initial 8” wafer. First, the silicon dioxide above the sensor area is thinned by plasma etching. This reduces the distance from a magnetic bead to the sensor surface, increasing the signal. The etch depth is controlled by comparison with etch reference marks implemented in the standard metallization, resulting in a final oxide thickness of approximately 2µm. Next, a thin layer (50nm/150nm) of Cr/Au is patterned over the sensor area using lift-off. Other materials were tested for protein adsorption, including Ti and Cu, but were found to be less effective than Au. However, Cu may represent a significant advantage in processing simplicity for CMOS processes that use Cu for metalization.

**Packaging and Experimental Setup**

Processed chips, including metallization extension, are assembled as drawn in Figure 7.

![Figure 7](image)

**Figure 7.** Simplified assembly diagram of sensor chip with fluid container and printed circuit board.

9mm x 15mm chips are mounted on a 15-cm long PCB. A 300-µL polystyrene vial is inverted and epoxied to the silicon chip. A small hole is predrilled in the vial to allow fluid entry. The PCB connects to an analog processing board via RJ45 connector. A 1-kHz, 30-kA/m magnetic field is applied parallel to the sensor surface by a custom C-core electromagnet (Figure 8).

![Figure 8](image)

**Figure 8.** Sensor chip and assembly mounted in the C-core electromagnet.

This magnet is mounted on rubber bushings so that it can be adjusted to reduce leakage normal to the sensor surface. The 250-kHz electrical modulation signal is sinusoidal and is applied to both the gate of the active sensing device and the input of the mixer IC. The balanced, current-mode output of the post amplifier is converted to a voltage on the analog processing PCB and amplified by instrumentation amplifiers. A simplified diagram of the signal processing is shown in Figure 9. Data is processed automatically in Matlab (The Mathworks, Natick, MA). The signal is digitally demodulated into in-phase and quadrature baseband components. These baseband signals are filtered by a FIR low-pass filter before reconstruction into polar form.

![Figure 9](image)

**Figure 9.** Simplified diagram of the signal processing path. Digital signal processing is performed in software.

The noise bandwidth can be adjusted by controlling the FIR filter bandwidth.
RESULTS

Anti-Hu IgG Assay

The sensor chip surfaces were coated overnight with 10 µg/ml human IgG diluted in phosphate buffered saline (PBS). Surfaces were blocked with 3% Non-Fat Dry Milk for 1 hour and washed 3 times in PBS with 0.5% Tween-20 (PBS-T). Either biotinylated goat anti-human IgG (200pM) or biotinylated goat anti-mouse IgG (as a control) (Sigma Aldrich, St. Louis, MO) was added to separate vials and incubated for 30 minutes. The samples were then washed 3 times with PBS and streptavidin-coated magnetic beads (5-8um diameter, Chemegen, Germany) diluted 1:200 to 125µg/ml were added and allowed to settle for 20 minutes. A rare-earth magnet was placed approximately 8mm above the sensor surface for 60 seconds before measurement. The magnet position was determined empirically. The sample was then placed in the measurement system. Figure 10 shows the response from the first 96 sensor elements for the goat anti-human IgG shown in grey and the goat anti-mouse IgG shown in black.

Figure 10. Relative output signal magnitude vs. array element index for anti-Hu IgG target protein (grey) and control (black). The average signal-to-noise ratio is approximately 13dB.

This data was collected prior to the use of electrical modulation. Recent experiments indicate that use of electrical modulation improves SNR by approximately 10-20dB. This improvement in SNR can be used to reduce the scan time for the array. Scanning all 256 elements takes approximately 2 minutes. The scanning time for the array becomes important if the device is scanned repeatedly while the magnetic washing force is ramped. This washing method may provide additional information about assay binding characteristics.

CONCLUSIONS

We have demonstrated an immunoassay platform with clinically relevant sensitivity, fabricated in a CMOS process. The noise and sensitivity limitations that traditionally limit the applicability of CMOS Hall sensors have been mitigated through architectural and signal processing techniques. With these improvements, the CMOS substrate may provide a cost-effective and easily manufactured platform for diagnostics. Furthermore, the proposed assay platform is potentially compact and automated, making it applicable to in-field applications.

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