ABSTRACT
This paper presents a low thermal budget surface micromachining process for piezoelectric micromachined ultrasonic transducers (PMUT) arrays with in-situ vacuum sealed cavities and high fill-factor. Low-temperature PECVD amorphous silicon (a-Si) and CMOS compatible aluminum nitride (AlN) are used in this process, making it possible to fabricate PMUT arrays on top of CMOS wafers directly at a low cost. The presented square PMUT array has a high fill-factor of 79% with a resonant frequency at 5.5 MHz in Fluorinert (FC-70, 3M), suitable for medical imaging applications. The transmitted surface pressure of this array is 2.93 kPa/V, 1.25x higher than previously reported PMUT arrays for fingerprint sensors and the receiving sensitivity is 0.5 V/kPa with a 10 V/pC charge amplifier. The presented fabrication process also showed tunability of PMUT 3-dimension (3D) profile by controlling the AlN residual stress in the deposition process and the possibility to increase the dynamic responses. Meanwhile, the reliability of the PMUTs was tested for 21 hours continuous driving and was tested for seal quality when immersed in Fluorinert.

INTRODUCTION
Ultrasonic sensing for medical and biometric identification applications have been developed based on the recent research efforts with micromachined ultrasonic transducers (MUT) [1–3]. Both capacitive (CMUT) and piezoelectric micromachined ultrasonic transducers (PMUT) have been demonstrated as an alternative to current bulk piezoelectric ultrasonic transducers due to its small size, low cost for array fabrication, and CMOS integration compatibility. Cavities are essential for all MUT structures. Ideally, vacuum sealed cavities can eliminate the squeeze film damping during MUT operation and improve efficiency [4]. For CMUT, a small gap cavity is required to achieve high sensitivity [5]. Meanwhile, the small gap impedes a large displacement amplitude. For PMUT, the cavity gap can be made large. Currently available process to achieve this gap includes backside etching [6], utilizing cavity SOI wafers [7], wafer bonding [8] and epitaxial encapsulation [9]. Backside etching requires substrate thinning process and deep reactive etching (DRIE) from the backside of the wafer. The finished structure can be fragile due to the most released areas of the cavities. The cavities are exposed to the atmosphere and generate additional acoustic damping during the MUT operation. Cavity SOI wafers can provide vacuum sealed cavities but are limited by a minimum spacing of 20 µm which is large for high-frequency PMUT applications. Wafer bonding process was utilized in prior art fingerprint sensors. The process is expensive, unreliable and needs even larger bonding areas (>400µm²) limiting the fill-factor of high-frequency PMUT arrays [10]. Here we present a surface micromachining process to fabricate PMUT arrays with high fill-factor, high yield, vacuum sealed cavities, and reliable structures with high performance.

FABRICATION PROCESS
Figure 1 illustrates the four-mask PMUT surface micromachining process to achieve high fill-factor. The process starts by depositing a 500 nm thick silicon dioxide (SiO₂) as an insulation layer. Then a 500 nm thick PECVD a-Si was deposited and patterned with the 1st mask to form the cavity sacrificial layer. The surface roughness of the SiO₂ and a-Si is important for the subsequent aluminum nitride layer depositions. The minimum spacing between a-Si patterns defines the fill-factor and can be made as small as 5 µm here, 4x smaller than the limits in commercially available cavity SOI wafers. The thickness of the a-Si layer defines the cavity depth. 500 nm thick is large enough for PMUT immersed applications like medical imaging or fingerprint sensors. A 500 nm AlN was firstly sputtered with an optimized recipe for achieving a lowest possible residual stress. The AlN layer was then patterned with the 2nd mask to open the release holes. The diameter of the releasing holes can vary from 2 µm to 10 µm depends on cavity sizes. Then the a-Si sacrificial layer was removed in xenon difluoride (XeF₂) gas within 50 cycles. Each cycle lasts for 30 seconds and the XeF₂ pressure is around 3.5 Torr. The minimum cavity spacing distance can be further reduced from 5 µm if the anchors can sustain the released membranes. After the a-Si were completely removed, another 500 nm AlN, 150 nm molybdenum (Mo) and a 1 µm AlN were sputtered in the same tool without breaking the vacuum. This deposition provided the in-situ vacuum sealed cavities due to the high vacuum in the chamber in the deposition process. The purpose of sputtering the bottom AlN layer in two steps is to provide a robust seal. The top 1 µm AlN deposition recipe is optimized to achieve a highly c-axis oriented crystalline structure for good piezoelectric responses. Then the 3rd mask is used to etch the top AlN layer and open the vias to bottom Mo electrode. In the last step, the top aluminum (Al) electrode with 300 nm thickness was evaporated and patterned with the 4th mask in a lift-off process.

FABRICATION RESULTS
The crystalline structure of the top AlN thin film is the key to
achieve good transmitting and receiving performance, and it was tested using X-ray diffraction (XRD, Siemens D5000 X-ray Diffractometer). Figure 2 shows the normal coupled 0-20 scan results. Single AlN (002) peak is shown without other orientations, indicating that the film is c-axis oriented. Inset figure is the rocking curve measurement results at AlN (002) peak. The full-width-half-maximum (FWHM) angle is 1.9°, indicating the AlN thin film is well oriented and predicting a good piezoelectric property.

This fabrication process allows us to create a 3D PMUT profile with AlN residual stress engineering. The same AlN sputtering recipe was used to deposit the bottom 1 µm AlN thin film with a target of lowest residual stress on two wafers. Then different AlN sputtering recipes were used to deposit the top 1 µm AlN thin films with different residual stresses. Therefore, different stress gradients were created on the top AlN in two wafers. The static deformation of the PMUTs was measured using a confocal microscope (Olympus LEXT OLS4000) and the results are shown in Figure 3. Figure 3(a) shows the normal coupled scan results. A 2D symmetric finite element model (FEM) was created in COMSOL to study the effect of top AlN residual stresses. We assume that the top AlN thin film has more compressive stress than the bottom AlN thin film. Figure 3(b) shows the opposite results. A 2D symmetric finite element model (FEM) was created in COMSOL to study the effect of top AlN residual stresses. We assume that the bottom AlN thin film was stress-free and the top AlN thin films had residual stresses varying from -100 MPa to +100 MPa. Figure 4(a) is showing the static deformation caused by the stress variation. The results indicated that a tensile residual stress generated concave PMUT profile and a compressive residual stress generated convex PMUT profile as expected. The PMUT bending amplitude is proportional to the absolute value of the top AlN residual stress. The simulation results are consistent with the results shown in Figure 3. Figure 4(b) is showing the dynamic responses of the PMUT with top AlN thin films of varying residual stresses. The displacement amplitude increases with the increase of the absolute value of the residual stress. The signs of the residual stress do not influence the dynamic performance.

An optical microscopic image of the fabricated square PMUT array is shown in Figure 5. The PMUT cavity is 50 µm wide and the electrode is 35 µm wide, targeting a ~ 5 MHz resonance in Fluorinert. The anchors are marked in the figure with yellow color. The releasing hole of a diameter of 10 µm is marked in the figure with blue color. The fill factor of this PMUT array is 79%, the highest value for PMUT arrays to our best knowledge. In each element, there are 3 x 20 PMUT cells inside and are connected in parallel to increase the output charge/current. The number of the cells can be adjusted for electrical matchings with the transmitting or receiving circuitry. Five elements are fabricated here and the pitch between each element is ~ 230 µm.

A focused ion beam (FIB, Scios Dual Beam SEM/FIB system) was used to open a trench of the approximate dimension of 50 µm wide, 20 µm long and 5 µm deep. The scanning electron microscopic (SEM) image of the cross-section is shown in figure 6. The two anchors and the cavities in between are shown in the figure. The AlN/Mo/AlN thin films stack is dense and continuous without any cracking. This shows that the cavity is properly sealed with the current fabrication process. A zoom-in figure of the SEM shows the
dense columnar structure of the AlN thin films with Mo electrode and the cavity. An interface of the bottom AlN can be seen in the figure and this is due to the different sputtering steps as mentioned earlier in the fabrication process.

CHARACTERIZATIONS

Dynamic responses

13 PMUT dices are selected across the wafer and the frequency responses are tested in the air with a laser Doppler vibrometer (LDV, OFV 512 and OFV 2700, Polytech) in conjunction with a network analyzer (E5601B, Agilent Technology). The results are plotted in figure 7. Inset figure is showing the positions of the 13 tested dices. The average displacement amplitude is ~36 nm/V, the average resonant frequency is ~11 MHz and the average quality factor is ~142. The displacement amplitude is 1.6x higher than circular Sc0.15Al0.15N PMUT as reported in [7]. The variation of the resonant frequency is ~1.8 MHz and the probable reason for this change is the AlN residual stress variation across the wafer. Further efforts on better controlling the AlN stress across the wafer is needed to reduce the resonant frequency variation. The tested 13 dices all indicated high displacement amplitude is showing a high yield from this surface micromachining process.

Reliability

A circular PMUT with 35 μm radius was selected from the wafer and continuously driven in air at the resonant frequency, 8.5 MHz, with 2.5 Vpp sinusoid voltages for 21 hours. The displacement amplitude of the PMUT was continuously monitored via LDV. Figure 8(a) shows the frequency response of the PMUT before (blue) and after (red) this long time running tests. No obvious performance decay was observed from the figure. The testing results showed that the fabricated PMUTs are robust and can endure long working times without performance degradation. The negligible change in the frequency response also indicated that no structural damaged during this testing process.

Another circular PMUT with the same geometry was also tested for the cavity seal quality. The frequency response of this PMUT was first measured and the results are indicated in Figure 8(b) in blue. The PMUT dice was then immersed in Fluorinert for one hour. After the immersion, the dice was picked out and air dried to remove the surface attached Fluorinert liquid. In the end, the PMUT frequency response was measured again and the results are shown in the red line in figure 7(b). The frequency response before and after immersion overlapped and no obvious resonant frequency change or amplitude degradation was observed from this test. If there were leakage happened during this testing, the resonant frequency would be lower, and the quality factor would be smaller due to the added liquid damping induced by the liquid leakage. The results showed that the cavity is well sealed, and no leakage was found in this case.

Pressure measurement
One element of the square PMUT array was used to test its transmitting sensitivity. The PMUT element was immersed in Fluorinert and driven by a high voltage pulser (Microchip, HV7355DB1) with 3 unipolar pulses at 5.5 MHz with 30 Vpp. A hydrophone (Precision acoustics) with 75 µm diameter was used to measure the transmitted acoustic pressure on top of the PMUT surface. Figure 9 shows the measured acoustic pressure amplitude was ~ 1.3 kPa and it was measured 3.6 µs after the pulses were generated. The measured time corresponding to the relative distance between the hydrophone and the PMUT surface. The hydrophone was lifted gradually so that the output pressure at different heights were measured and results are shown in figure 10. The measured pressure amplitudes were fitted with the following equation, $p(x) = 0.0058x^{-0.92}$, where $x$ is the acoustic path distance. The Rayleigh distance of this element is ~ 67.6 µm and the output surface pressure is ~ 2.93 kPa/V, 1.25x higher than reported in [11].

**Pulse-echo measurement**

The pulse-echo measurement was conducted with one element used as a transmitter and a neighbor element is used as a receiver. The same pulser was utilized in this measurement with the same condition as mentioned in pressure measurement. The receiving element was connected to a charge amplifier (FEMTO, HQA-15M-10T) with a gain of 10 V/pC. The output pressure propagated to the surface of the Fluorinert and air and then reflected. The PMUT receiving voltage from the echo pressure was amplified and shown on an oscilloscope. The Fluorinert height was adjusted so that the relationship between PMUT receiving voltage and acoustic path distance can be obtained. The results are plotted in figure 11 and are fitting results $V(x) = 0.0029x^{-0.91}$. The receiving sensitivity can be obtained using $V(x)/p(x)$, which leads to 0.51 V/kPa. The imaging depth of this element is around 1 cm under 30 Vpp pulses and it can be increased with large element sizes, higher transmitting voltages, and driving more elements with beam forming for real medical imaging applications.

**CONCLUSION**

Here, we presented a low cost, low thermal budget, CMOS compatible surface micromachining process with four-mask and in-situ vacuum sealed cavities for fabricating high fill-factor PMUT arrays. The proposed fabrication process demonstrated PMUTs with high transmitting and receiving sensitivities, high yield, and high reliability. This fabrication process also enables us to tune the 3D geometry of the fabricated PMUT with either convex or concave structures without complicated etching steps as mentioned in [12] and improved the AlN piezoelectric properties. This fabrication process can be further improved with the potentials of using different sacrificial layer and elastic layer selections to create more robust, higher sensitivity PMUT structures and arrays.

**ACKNOWLEDGMENT**

The devices were fabricated in Berkeley Marvell Nanofabrication Laboratory. The authors thank Berkeley Sensor and Actuator Center (BSAC) industrial members for financial support.

**REFERENCES**


**CONTACT**

Q. Wang, tel: +1-530-746-1632; qixwang@ucdavis.edu